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Application No.: 10-2001-0003582

Title of the Invention: Method and apparatus for detecting violation of block boundary

According to Article 63 of the Korean Patent Law, the applicant is notified that the present application has been rejected for the reasons given below. Any Argument or Amendment which the applicant may wish to submit, must be submitted by May 24, 2002. An indefinite number of one-month extensions in the period for submitting a response may be obtained upon request, however no official confirmation of the acceptance of a request for an extension will be issued.

Reasons

The invention as claimed in claims 1 and 5 could have been easily invented by one of ordinary skill in the art prior to the filing of the application, and thus this application is rejected according to Article-29(2) of the Korean Patent Law.

The invention is directed to "a block boundary violation detecting apparatus and method" which ensures normal recoding through phase comparison at the boundary between the heading portion of an encoding block and an error correction code (ECC) block on a disk. The invention as claimed in claims 1 and 5 is characterized in that a block boundary signal is generated, the phase difference between the block boundary signal and a synchronous signal of the encoding block is detected to determine whether violation of a boundary occurs. However, the invention as claimed in claims 1 and 5 can be easily invented by one of ordinary skill in the art from Korean Utility-Model Registration No. 20-0141095 (published on May 1, 1999), which is referred to as "cited reference"

hereinafter. The cited reference is directed to a disk recording apparatus, a disk-type recording medium having predetermined absolute address information pre-written on a recording track and a data block having a block synchronization pattern of two symbols in a heading portion thereof in order to detect a boundary of the data block. The disk recording apparatus always monitors absolute address information on the recording track, detects errors based on the absolute address information, calculates the return time of a recording device to the heading position of a record unit during recording on the recording track if any error is detected, and controls a read location of an input memory control unit based on the calculated result to record data

However, the cited reference failed to disclose a process of generating the block boundary signal in detail. However, this process can be easily implemented by one of ordinary skill in the art using the absolute address information on the recording track, like generating the block boundary signal using the block address information pre-written on the disk in the present invention.

Therefore, the present invention as claimed in claims 1 and 5 could have been easily invented by one of ordinary skill in the art based on the cited reference.

Enclosure: Korean Utility-Model Registration No. 0141095 (published on May 1, 1999)

24 March 2003

Choong-Hee Han/Examiner
Information part
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